

ABSTRACT OF THE DISCLOSURE

DATA PROCESSING METHOD AND SYSTEM FOR SIMULATION OF HARDWARE FAULTS UTILIZING A PCI BUS

A data processing system and method are disclosed for simulating a hardware fault occurring on an expansion card coupled to the system utilizing a bus. The hardware fault to simulate is specified. A determination is made of a signal to output utilizing the bus to simulate the hardware fault occurring on the expansion card. The signal is output during operation of the expansion card such that the hardware fault occurring on the expansion card is simulated.